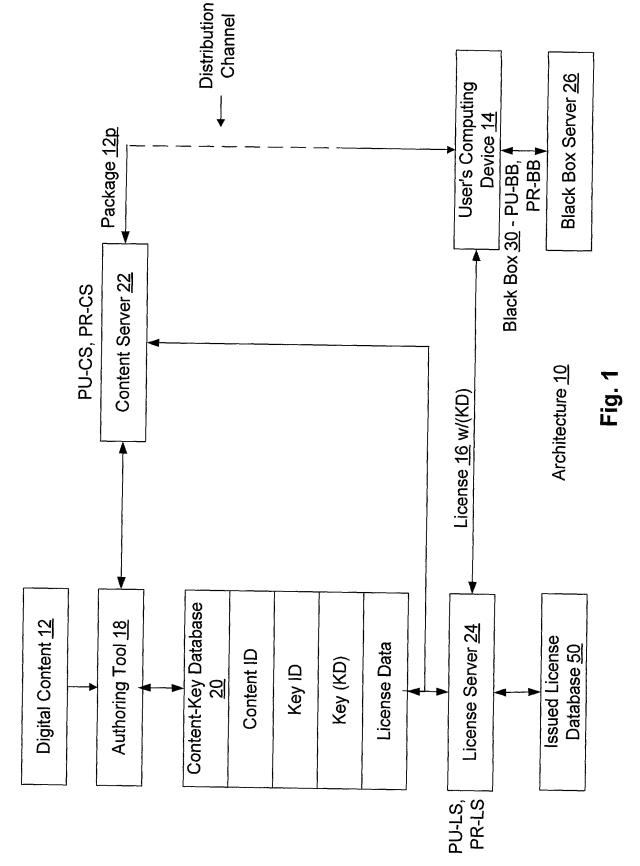
Title: Secure Processor Architecture For Use With A Digital Rights Management (DRM)

System On A Computing Device

Inventor(s): Marcus Peinado and Paul England

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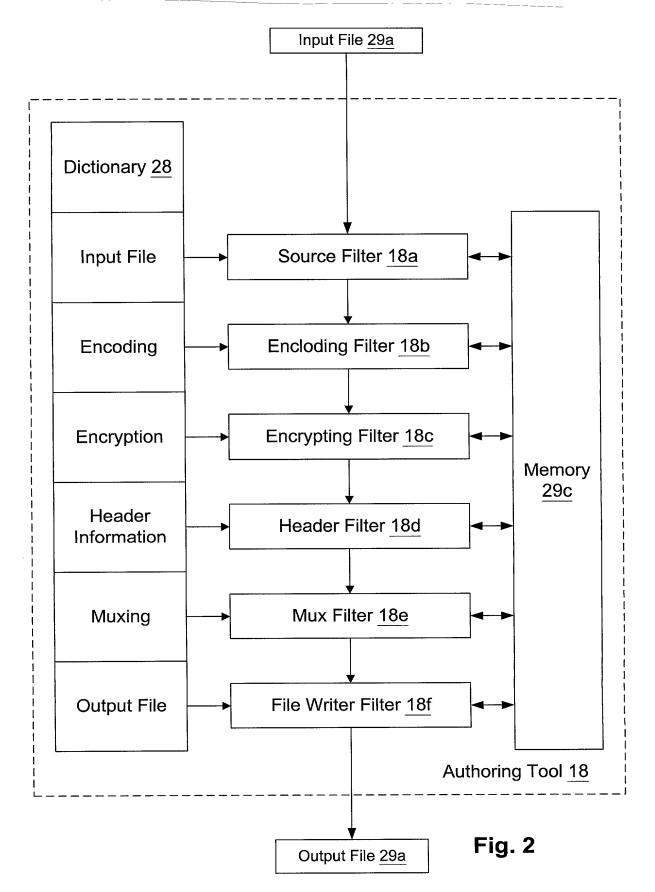


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Inventor(s): Marcus Peinado and Paul England

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Inventor(s): Marcus Peinado and Paul England

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Fig. 3

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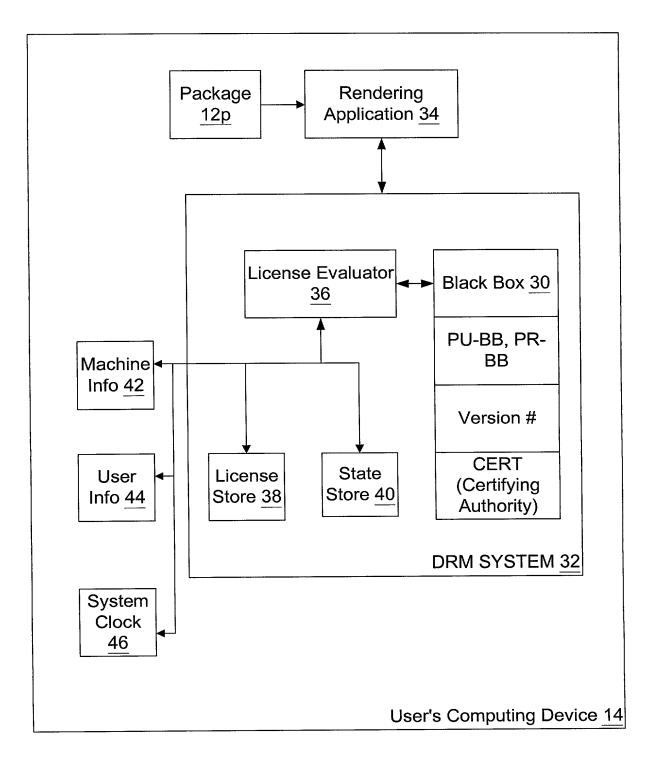


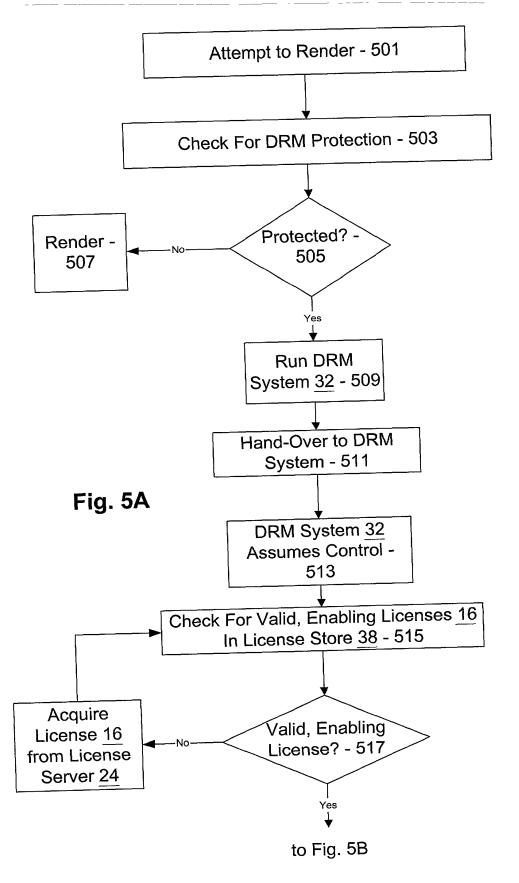
Fig. 4

Title: Secure Processor Architecture For Use With A Digital Rights Management (DRM)

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Inventor(s): Marcus Peinado and Paul England

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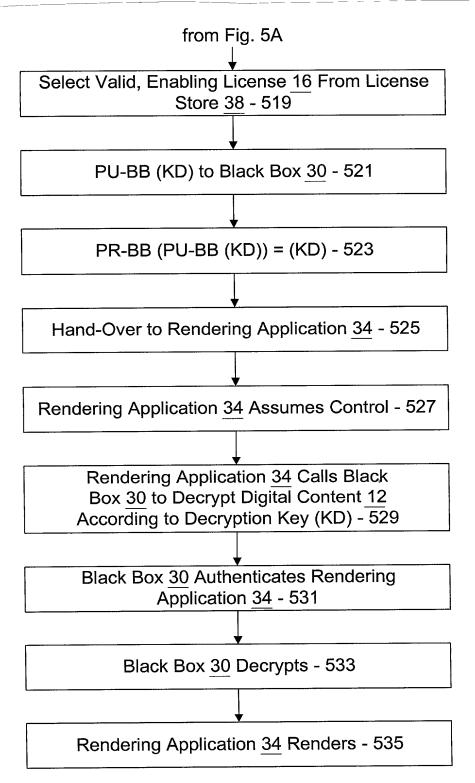


Fig. 5B

Title: Secure Processor Architecture For Use With A Digital Rights Management (DRM)

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Inventor(s): Marcus Peinado and Paul England

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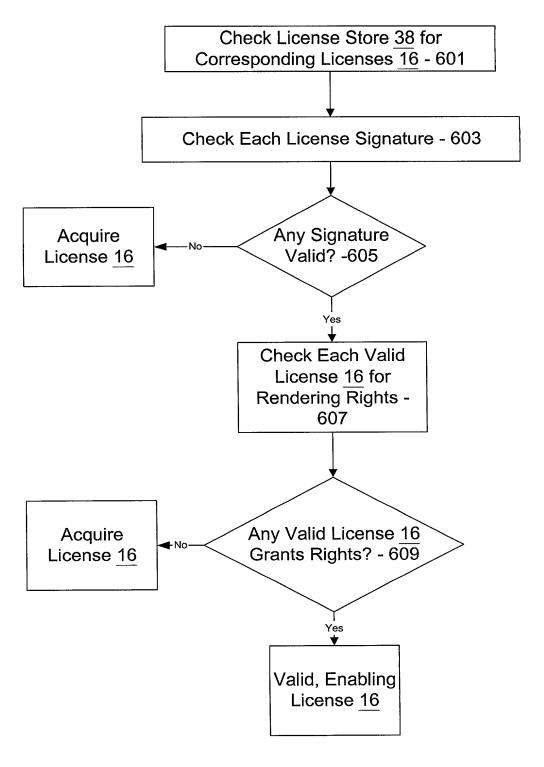


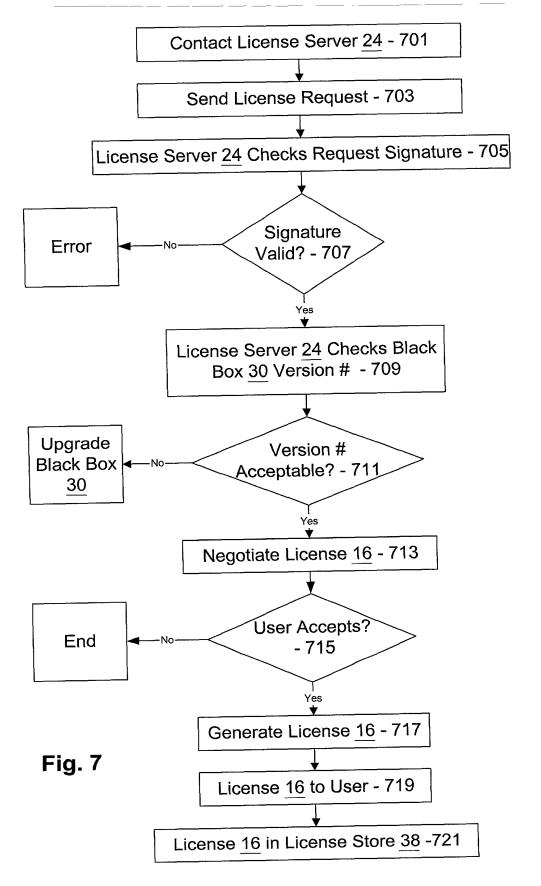
Fig. 6

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Title: Secure Processor Architecture For Use With A Digital Rights Management (DRM)

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Inventor(s): Marcus Peinado and Paul England

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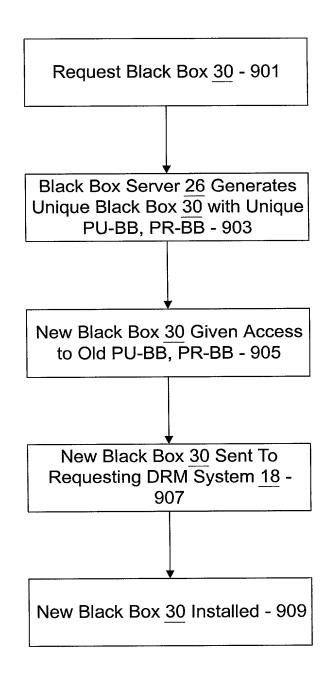


Fig. 9

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System On A Computing Device

Inventor(s): Marcus Peinado and Paul England

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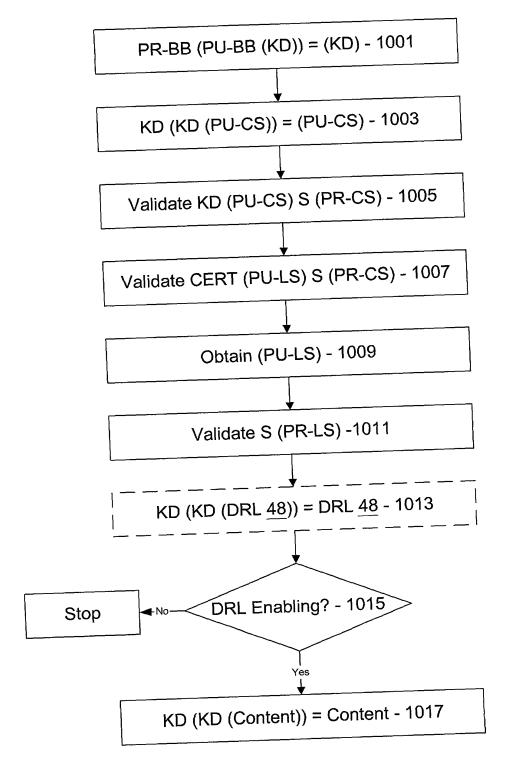


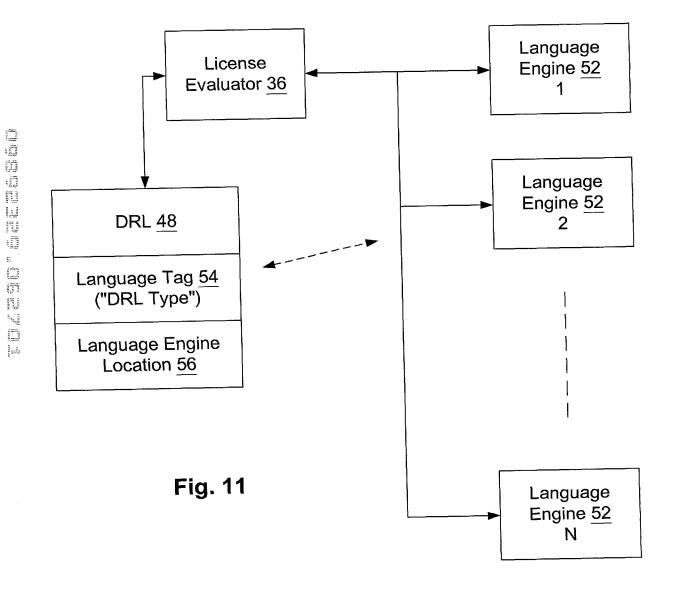
Fig. 10

Title: Secure Processor Architecture For Use With A Digital Rights Management (DRM)

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Inventor(s): Marcus Peinado and Paul England

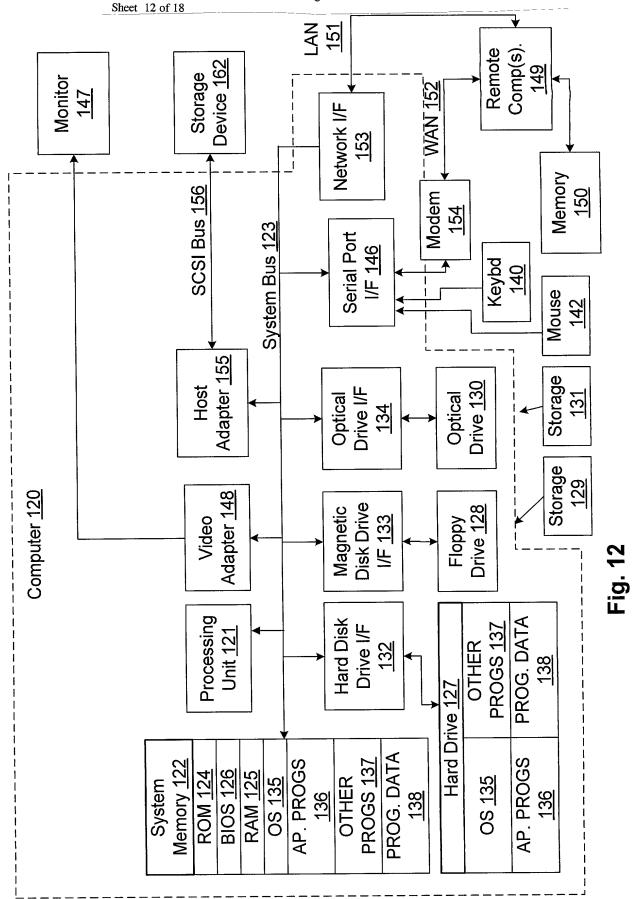
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Inventor(s): Marcus Peinado and Paul England



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Fig. 13

Title: Secure Processor Architecture For Use With A Digital Rights Management (DRM)

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Inventor(s): Marcus Peinado and Paul England

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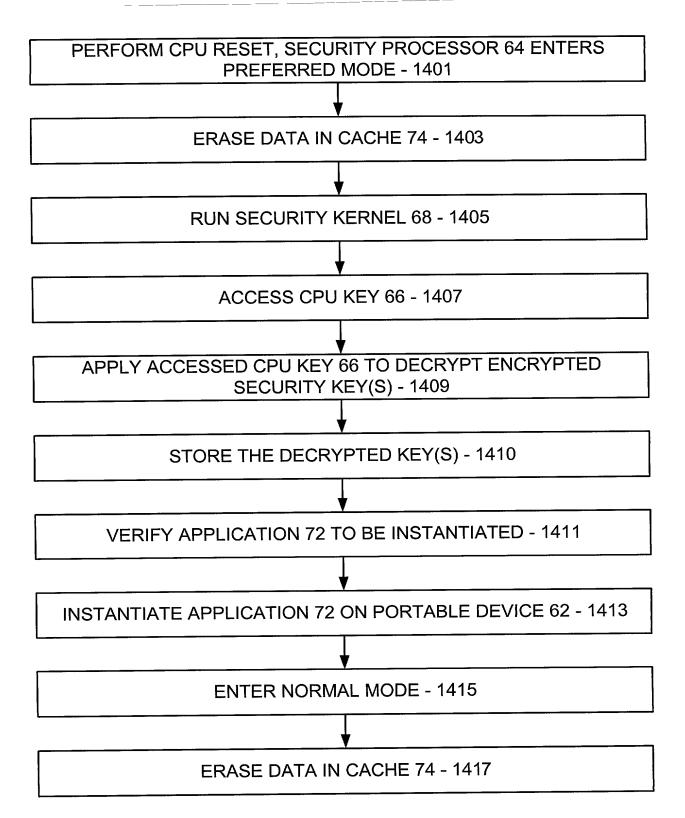


FIG. 14

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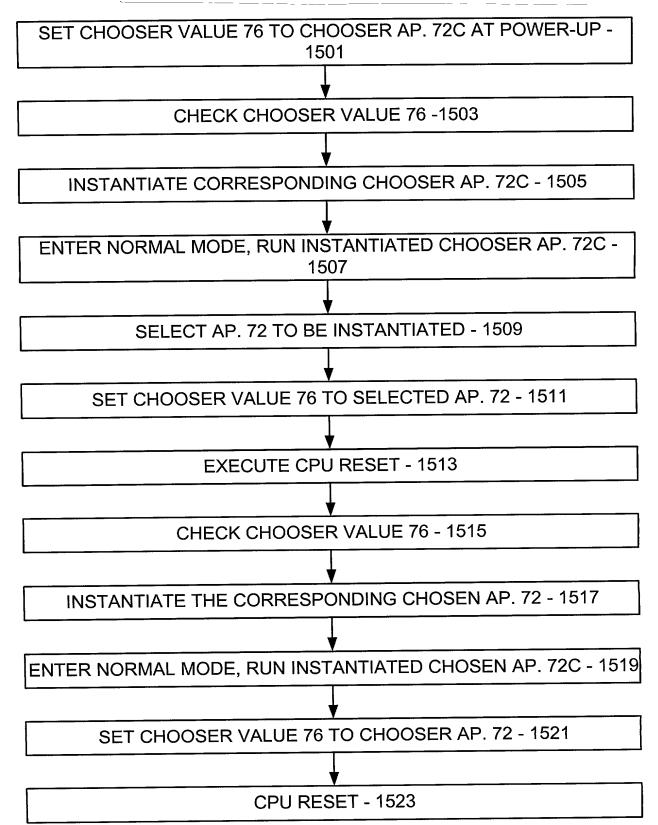


FIG. 15

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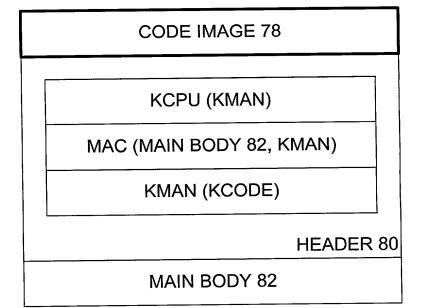


Fig. 16

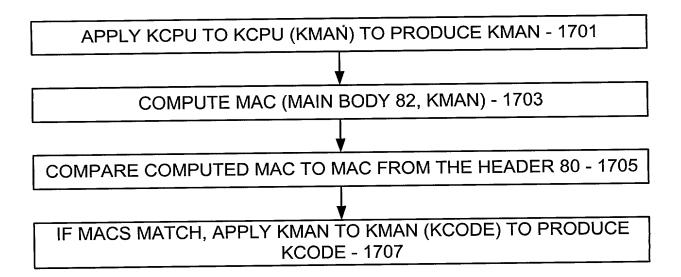


FIG. 17

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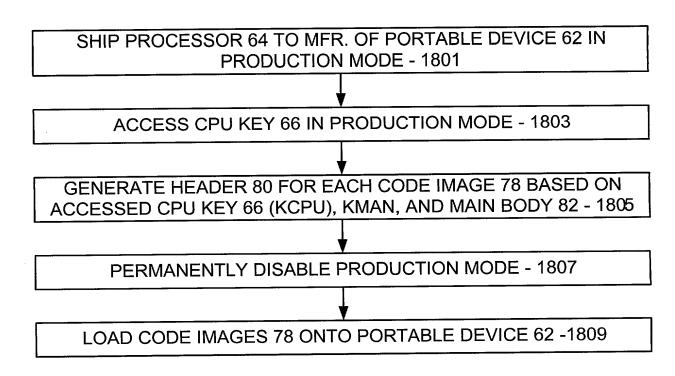


FIG. 18

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Title: Secure Processor Architecture For Use With A Digital Rights Management (DRM)

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Inventor(s): Marcus Peinado and Paul England

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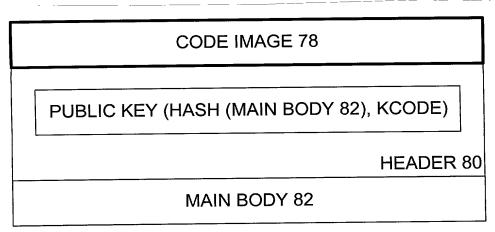


Fig. 19

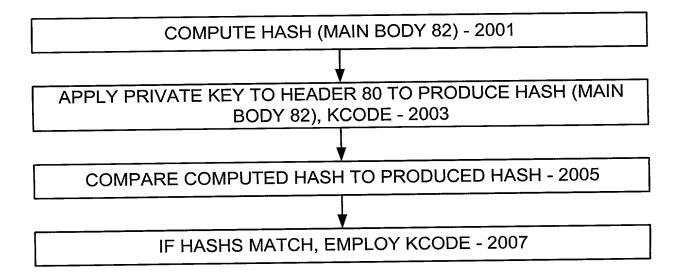


FIG. 20

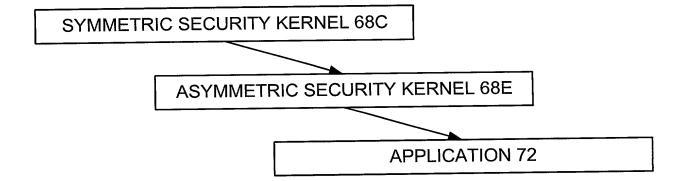


FIG. 21